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Motorola Q 9h Windows Mobile smartphone. Announced Feb 2007. Features 2.4" display, 2 MP primary camera, 1170 mAh battery, 96 MB RAM.

Motorola Q 9h - Full phone specifications

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This textbook serves as an introduction to the subject of embedded systems design, using microcontrollers as core components. It develops concepts from the ground up, covering the development of embedded systems technology, architectural and organizational aspects of controllers and systems, processor models, and peripheral devices. Since microprocessor-based embedded systems tightly blend hardware and software components in a single application, the book also introduces the subjects of data representation formats, data operations, and programming styles. The practical component of the book is tailored around the architecture of a widely used Texas Instrument's microcontroller, the MSP430 and a companion web site offers for download an experimenter's kit and lab manual, along with Powerpoint slides and solutions for instructors.

An accessible treatment of the modeling and solution of integer programming problems, featuring modern applications and software In order to fully comprehend the algorithms associated with integer programming, it is important to understand not only how algorithms work, but also why they work. Applied Integer Programming features a unique emphasis on this point, focusing on problem modeling and solution using commercial software. Taking an application-oriented approach, this book addresses the art and science of mathematical modeling related to the mixed integer programming (MIP) framework and discusses the algorithms and associated practices that enable those models to be solved most efficiently. The book begins with coverage of successful applications, systematic modeling procedures, typical model types, transformation of non-MIP models, combinatorial optimization problem models, and automatic preprocessing to obtain a better formulation. Subsequent chapters present algebraic and geometric basic concepts of linear programming theory and network flows needed for understanding integer programming. Finally, the book concludes with classical and modern solution approaches as well as the key components for building an integrated software system capable of solving large-scale integer programming and combinatorial optimization problems. Throughout the book, the authors demonstrate essential concepts through numerous examples and figures. Each new concept or algorithm is accompanied by a numerical example, and, where applicable, graphics are used to draw together diverse problems or approaches into a unified whole. In addition, features of solution approaches found in today's commercial software are identified throughout the book. Thoroughly classroom-tested, Applied Integer Programming is an excellent book for integer programming courses at the upper-undergraduate and graduate levels. It also serves as a well-organized reference for professionals, software developers, and analysts who work in the fields of applied mathematics, computer science, operations research, management science, and engineering and use integer-programming techniques to model and solve real-world optimization problems.

Hardware and Computer Organization is a practical introduction to the architecture of modern microprocessors. This book from the bestselling author explains how PCs work and how to make them work for you. It is designed to take students "under the hood" of a PC and provide them with an understanding of the complex machine that has become such a pervasive part of everyday life. It clearly explains how hardware and software cooperatively interact to accomplish real-

world tasks. Unlike other textbooks on this topic, Dr. Berger's book takes the software developer's point-of-view. Instead of simply demonstrating how to design a computer's hardware, it provides an understanding of the total machine, highlighting strengths and weaknesses, explaining how to deal with memory and how to write efficient assembly code that interacts directly with, and takes best advantage of the underlying hardware. The book is divided into three major sections: Part 1 covers hardware and computer fundamentals, including logical gates and simple digital design. Elements of hardware development such as instruction set architecture, memory and I/O organization and analog to digital conversion are examined in detail, within the context of modern operating systems. Part 2 discusses the software at the lowest level, assembly language, while Part 3 introduces the reader to modern computer architectures and reflects on future trends in reconfigurable hardware. This book is an ideal reference for ECE/software engineering students as well as embedded systems designers, professional engineers needing to understand the fundamentals of computer hardware, and hobbyists. The renowned author's many years in industry provide an excellent basis for the inclusion of extensive real-world references and insights. Several modern processor architectures are covered, with examples taken from each, including Intel, Motorola, MIPS, and ARM.

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