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safety

System
designers,
computer
scientists and
engineers have
c- tinuously
invented and
employed
notations for

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modeling, specifying, simulating, documenting, communicating, teaching, verifying and controlling the designs of digital systems. Initially these systems were represented via electronic and fabrication

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details. Following C. E. Shannon's revelation of 1948, logic diagrams and Boolean equations were used to represent digital systems in a fashion that de-emphasized

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electronic and
fabrication
detail while
revealing
logical
behavior. A
small number of
circuits were
made available
to remove the
abstraction of
these
representations
when it was

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desirable to do so. As system complexity grew, block diagrams, timing charts, sequence charts, and other graphic and symbolic notations were found to be useful in summarizing the gross features

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of a system and describing how it operated. In addition, it always seemed necessary or appropriate to augment these documents with lengthy verbal descriptions in a natural language. While each notation

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was, and still is, a perfectly valid means of expressing a design, lack of standardization, conciseness, and formal definitions interfered with communication and the understanding between groups

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of people using different notations. This problem was recognized early and formal languages began to evolve in the 1950s when I. S. Reed discovered that flip-flop input equations were equivalent to a register

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transfer
equation, and
that xvi tor-
like notation.
Expanding these
concepts Reed
developed a no-
tion that became
known as a
Register
Transfer
Language (RTL) .

More and more

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sailors and powerboaters are buying and relying on electronic and electric devices aboard their boats, but few are aware of proper installation procedures or how to safely troubleshoot

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these devices if they go on the blink.

This book describes the most frequently used high-speed serial buses in embedded systems, especially those used by FPGAs. These buses

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employ SerDes,
JESD204, SRIO,
PCIE, Aurora and
SATA protocols
for chip-to-chip
and board-to-
board
communication,
and CPCIE, VPX,
FC and
Infiniband
protocols for
inter-chassis
communication.

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For each type,
the book
provides the bus
history and
version info,
while also
assessing its
advantages and
limitations.
Furthermore, it
offers a
detailed guide
to implementing
these buses in

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FPGA design,
from the
physical layer
and link
synchronization
to the frame
format and
application
command. Given
its scope, the
book offers a
valuable
resource for
researchers, R&D

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engineers and graduate students in computer science or electronics who wish to learn the protocol principles, structures and applications of high-speed serial buses.

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This book provides glycoscientists with a handbook of useful databases that can be applied to glycoscience research.

Although many databases are now publicly available, one of the hurdles

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for their users
is the learning
curve required
to effectively
utilize those
databases.

Therefore, this
book not only
describes the
existing
databases, but
also provides
tips on how to
obtain the

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target data.

That is, because many databases provide a variety of data that could be obtained from different perspectives, each chapter provides users with potential biological questions that

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can be answered by a particular database and step-by-step instructions, with figures, on how to obtain that data.

Troubleshooting tips are also provided to aid users

encountering problems that

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can be predicted
when using these
databases.

Moreover,
contact
information for
each database is
provided in case
unexpected
issues arise.

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form the hub of
the world's
largest global
IT media
network.

This book
constitutes the
refereed contest
reports of the
20th

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International
Conference on
Pattern
Recognition,
ICPR 2010, held
in Istanbul,
Turkey, in
August 2010. The
31 revised full
papers presented
were carefully
reviewed and
selected. The
papers are

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organized in
topical sections
on BiHTR - Bi-
modal

handwritten Text
Recognition,
CAMCOM 2010 -
Verification of
Video Source
Camera

Competition, CDC
- Classifier
Domains of
Competence, GEPR

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- Graph

Embedding for

Pattern

Recognition,

ImageCLEF@ICPR -

Information

Fusion Task,

ImageCLEF@ICPR -

Visual Concept

Detection Task,

ImageCLEF@ICPR -

Robot Vision

Task, MOBIO -

Mobile Biometry

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Face and Speaker
Verification
Evaluation, PR
in HIMA -
Pattern
Recognition in H
istopathological
Images, SDHA
2010 - Semantic
Description of
Human
Activities.

30GS 3.0L in-

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line 4-cylinder
(135 HP), 43GL
4.3L V-6 (160
HP), 43GI 4.3L
V-6 (180 HP),
43GXI 4.3L V-6
(210 HP), 50GL
5.0L V-8 (220
HP), 50GI 5.0L
V-8 (250 HP),
50GXI 5.0L V-8
(270 HP), 57GS
5.7L V-8 (225
HP), 57GS 5.7L

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V-8 (250 HP),
57GSI 5.7L V-8
(280 H

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